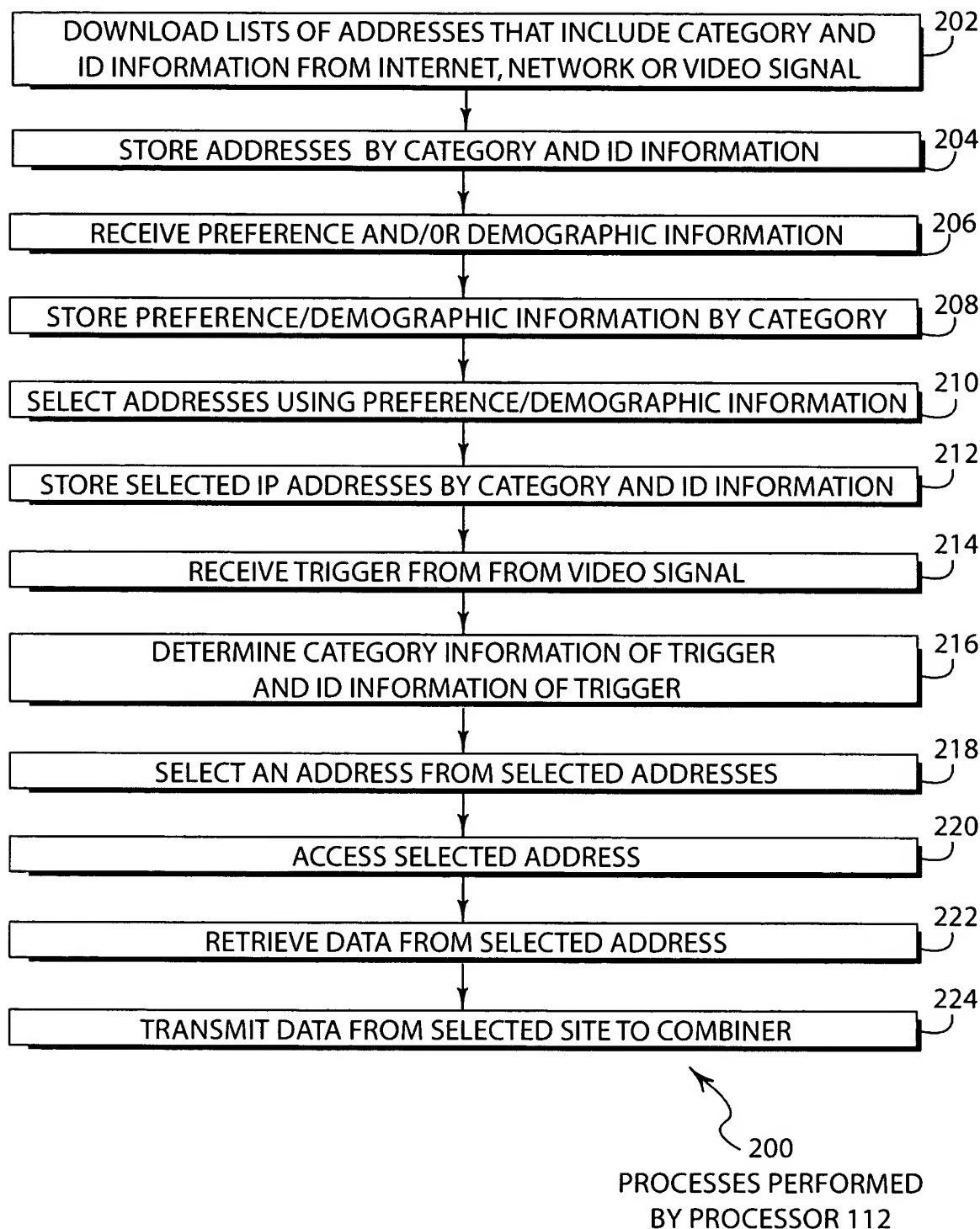
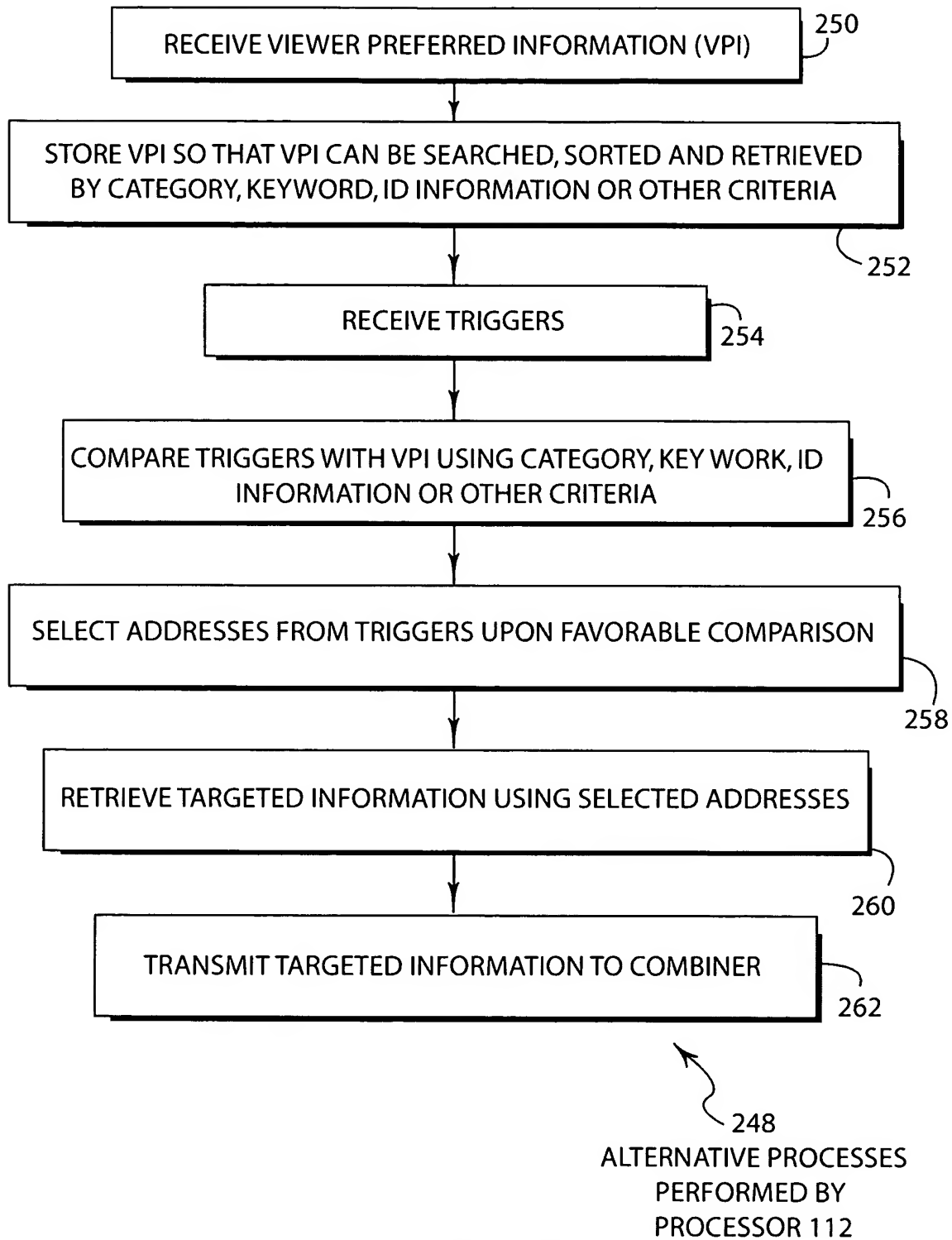
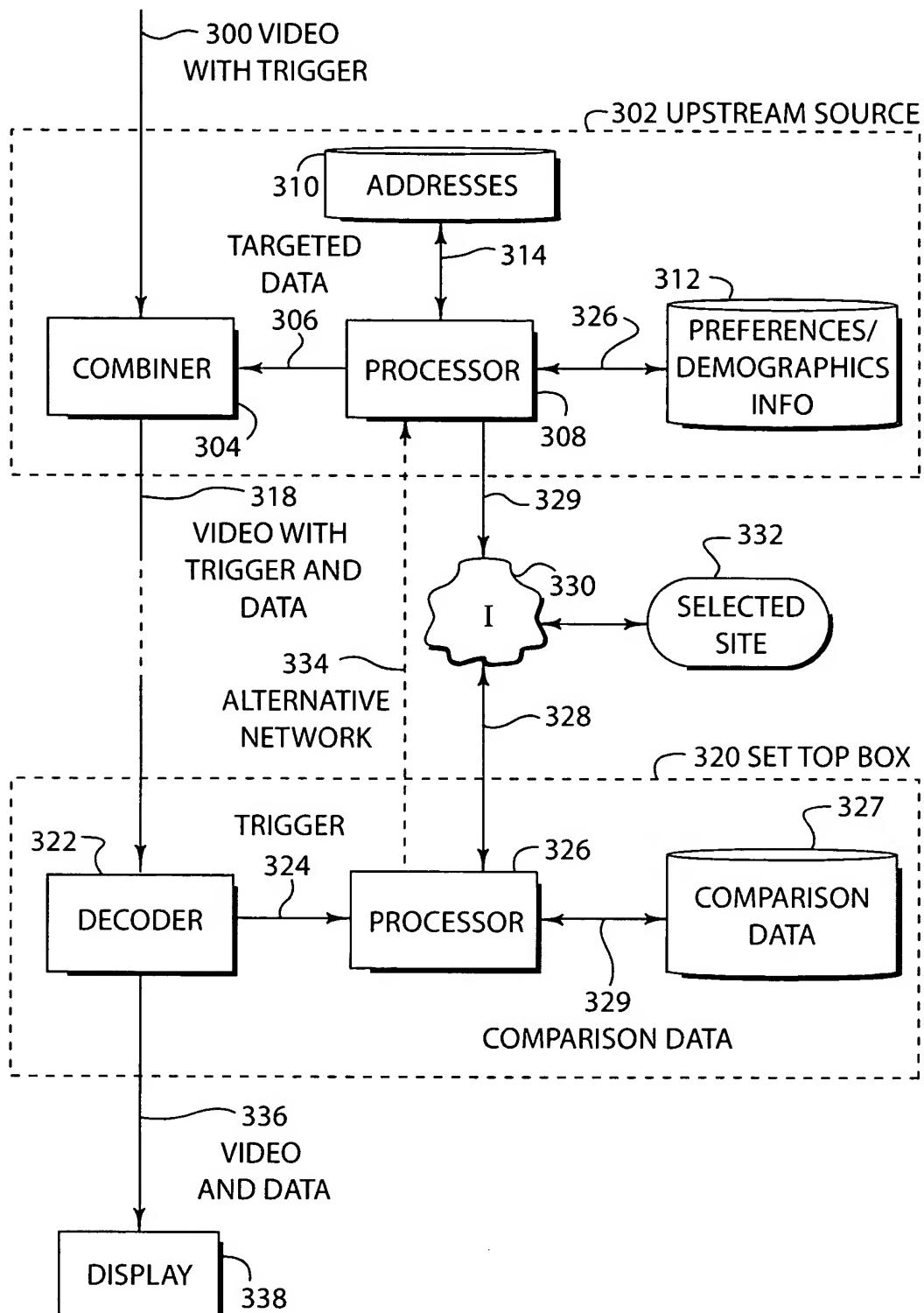
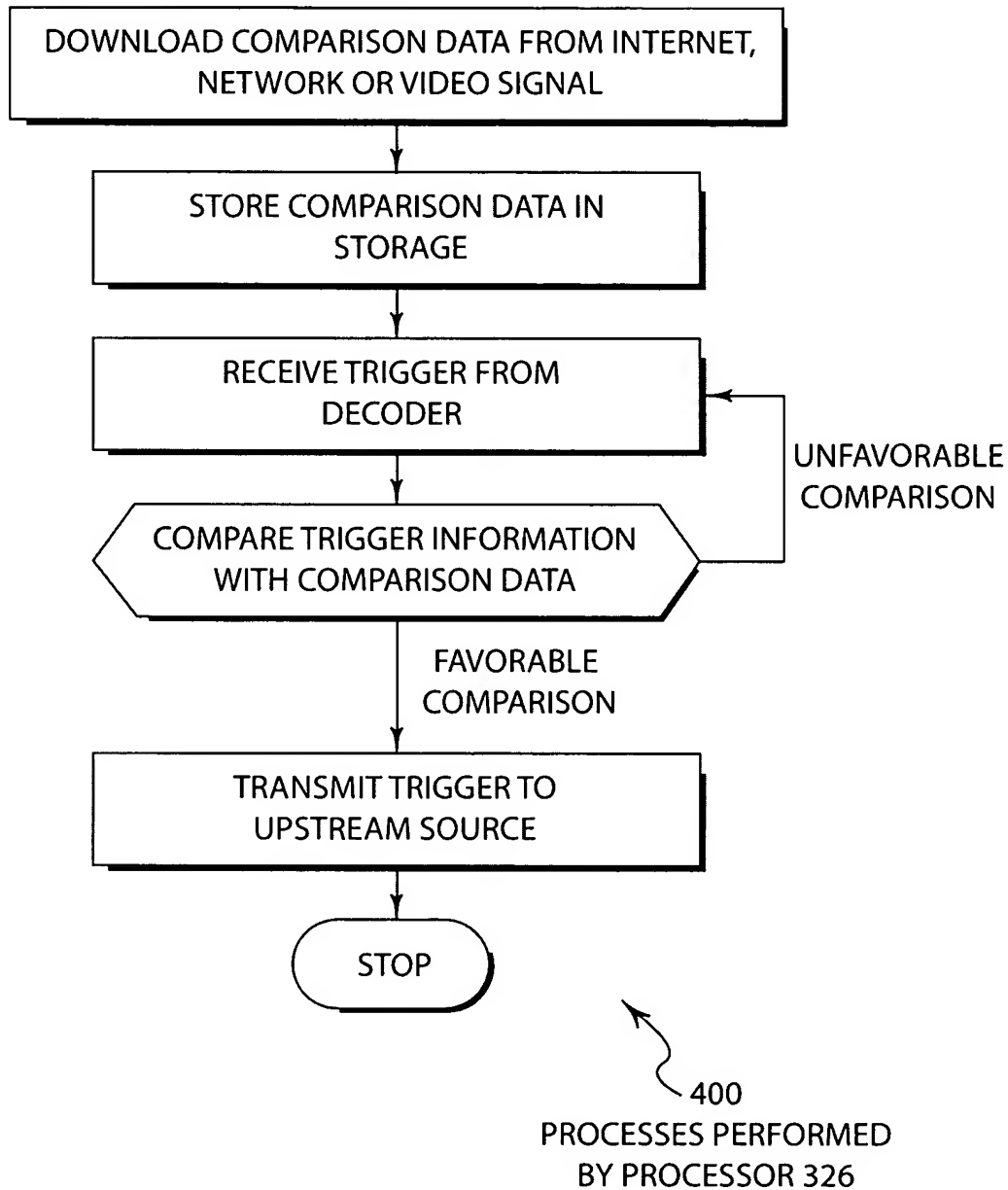


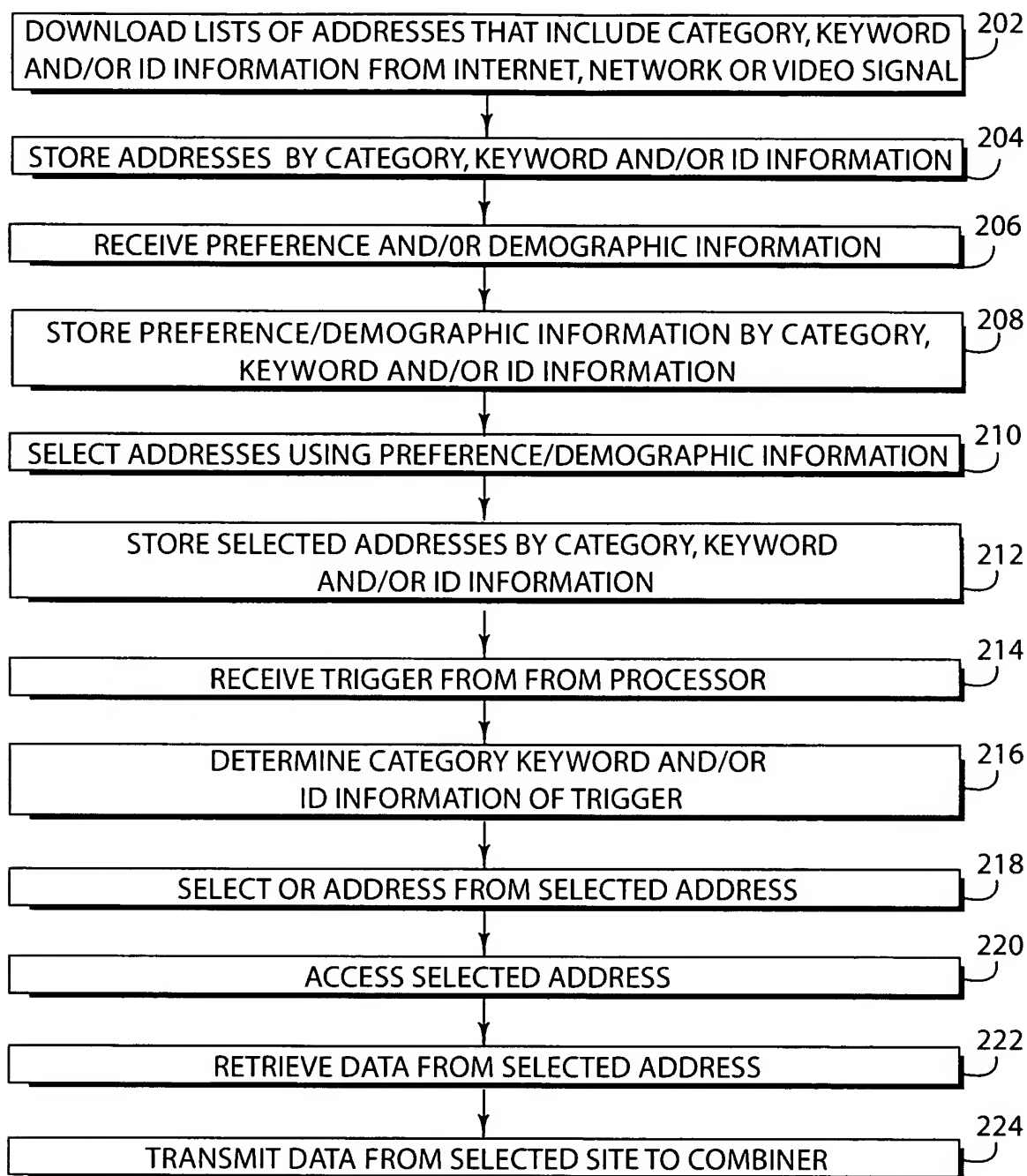
FIGURE 1

**FIGURE 2A**

**FIGURE 2B**

**FIGURE 3**

**FIGURE 4**



500

PROCESSES PERFORMED
BY PROCESSOR 308**FIGURE 5A**

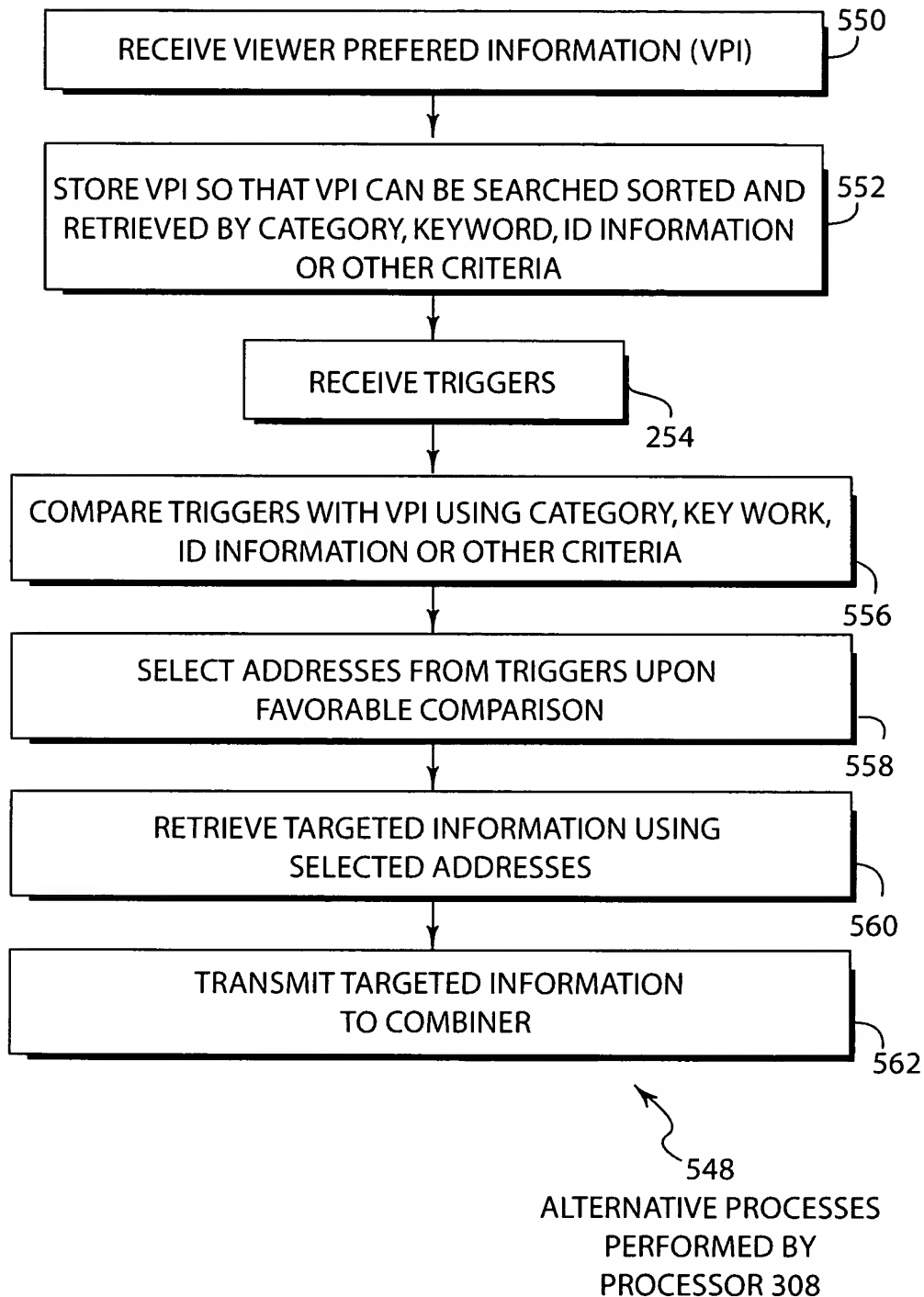
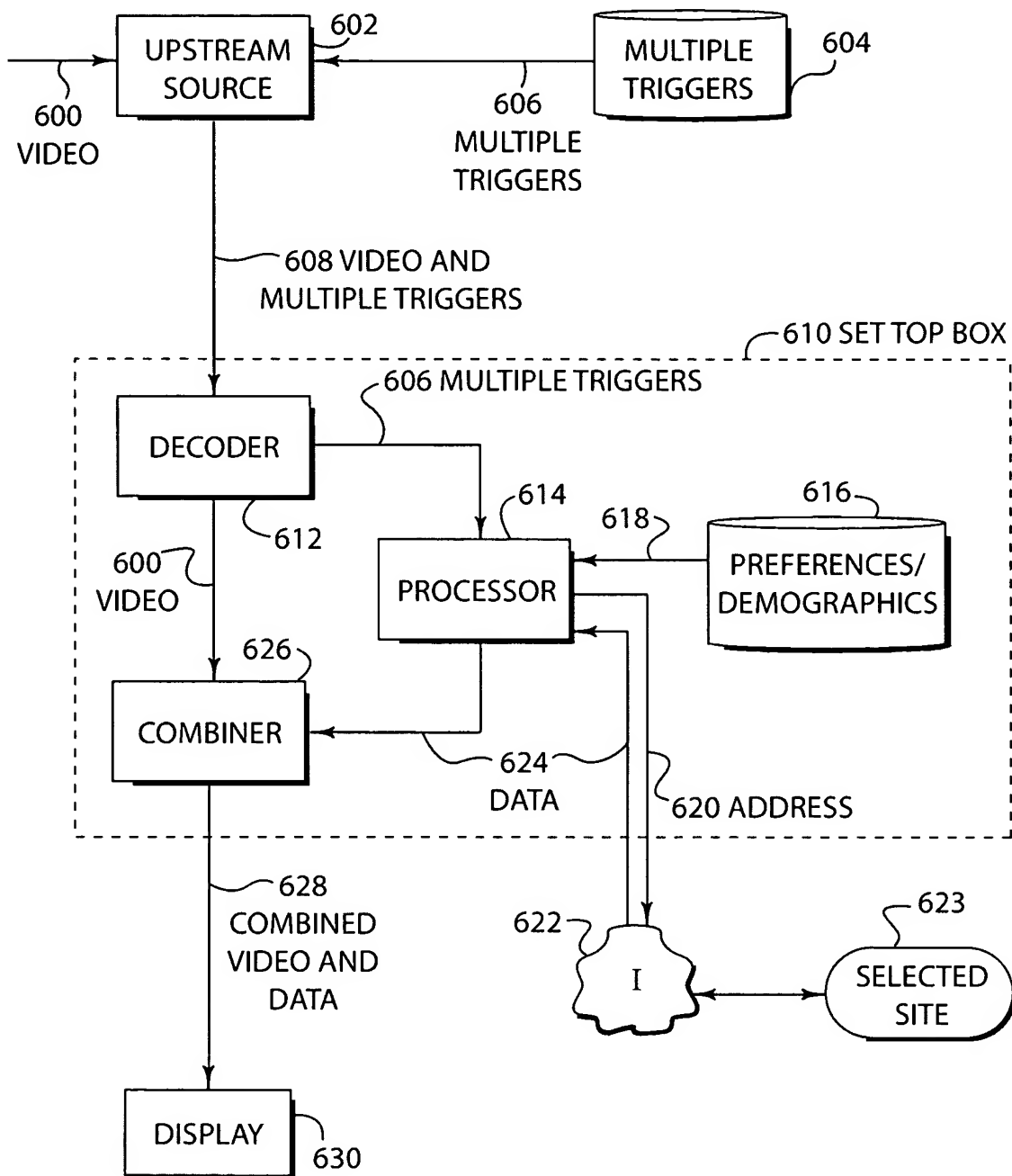
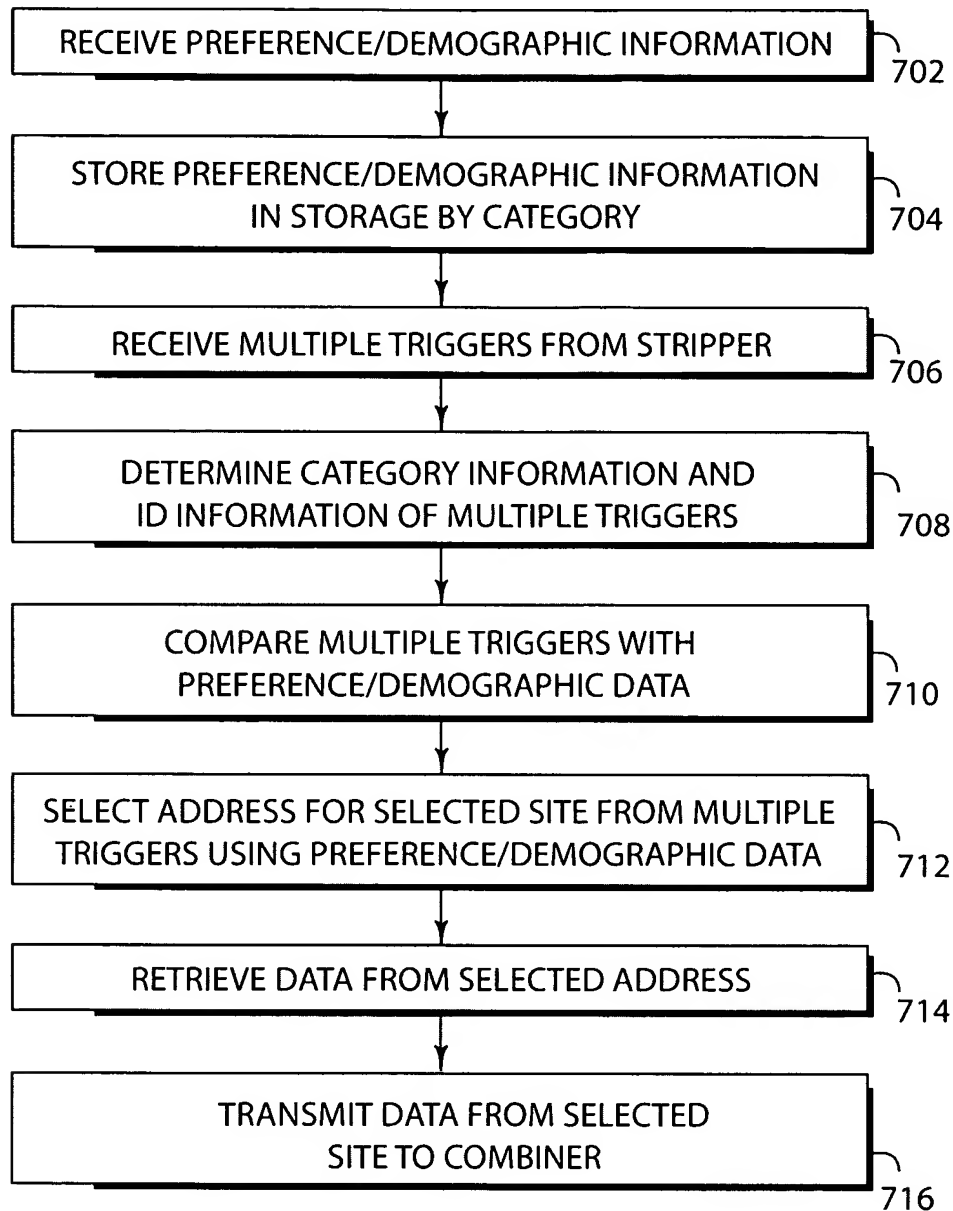


FIGURE 5B

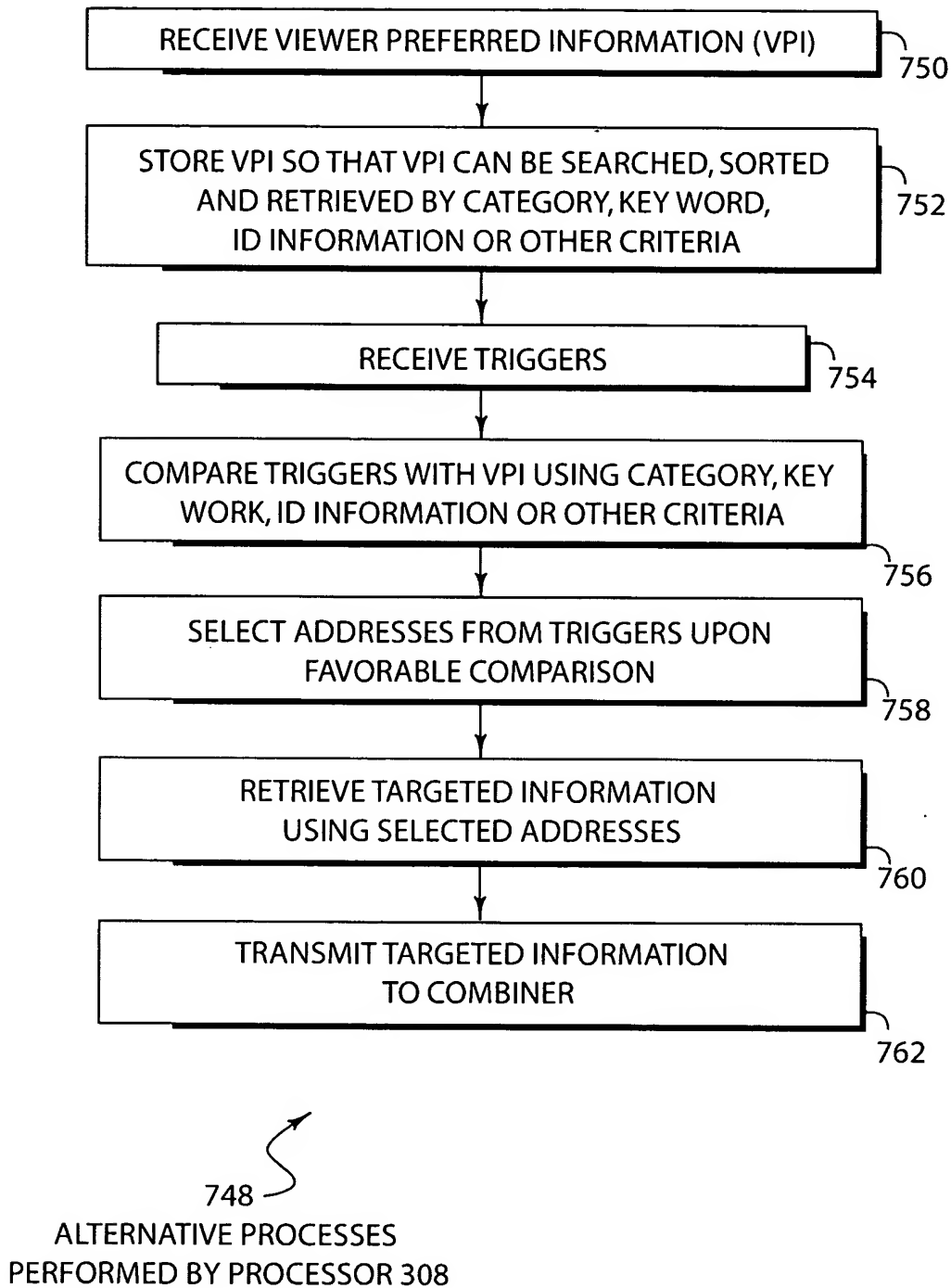
**FIGURE 6**



700

PROCESSES PERFORMED
BY PROCESSOR 714

FIGURE 7A

**FIGURE 7B**